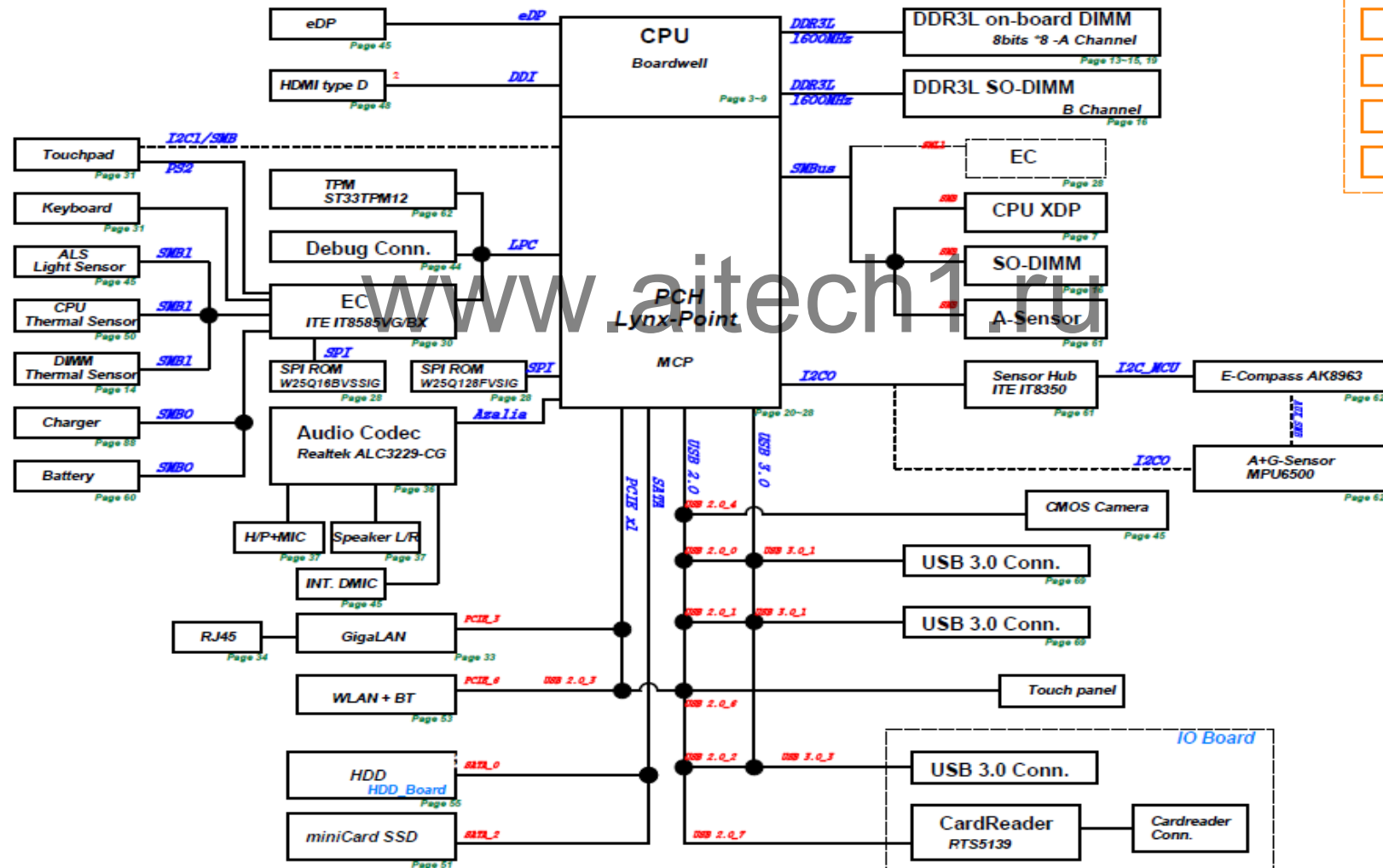


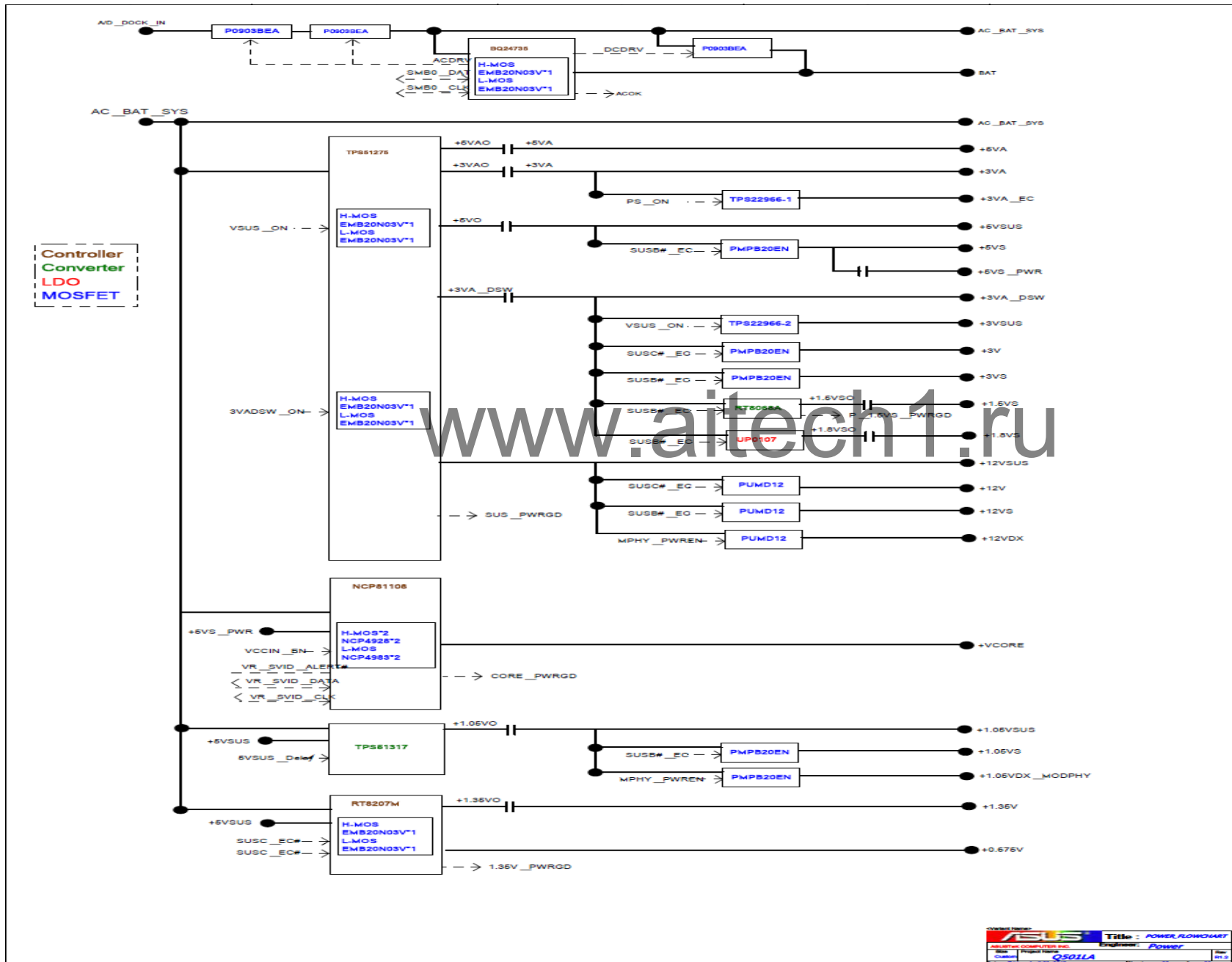
BLOCK DIAGRAM

Q502LA SCHEMATIC Revision R2.0

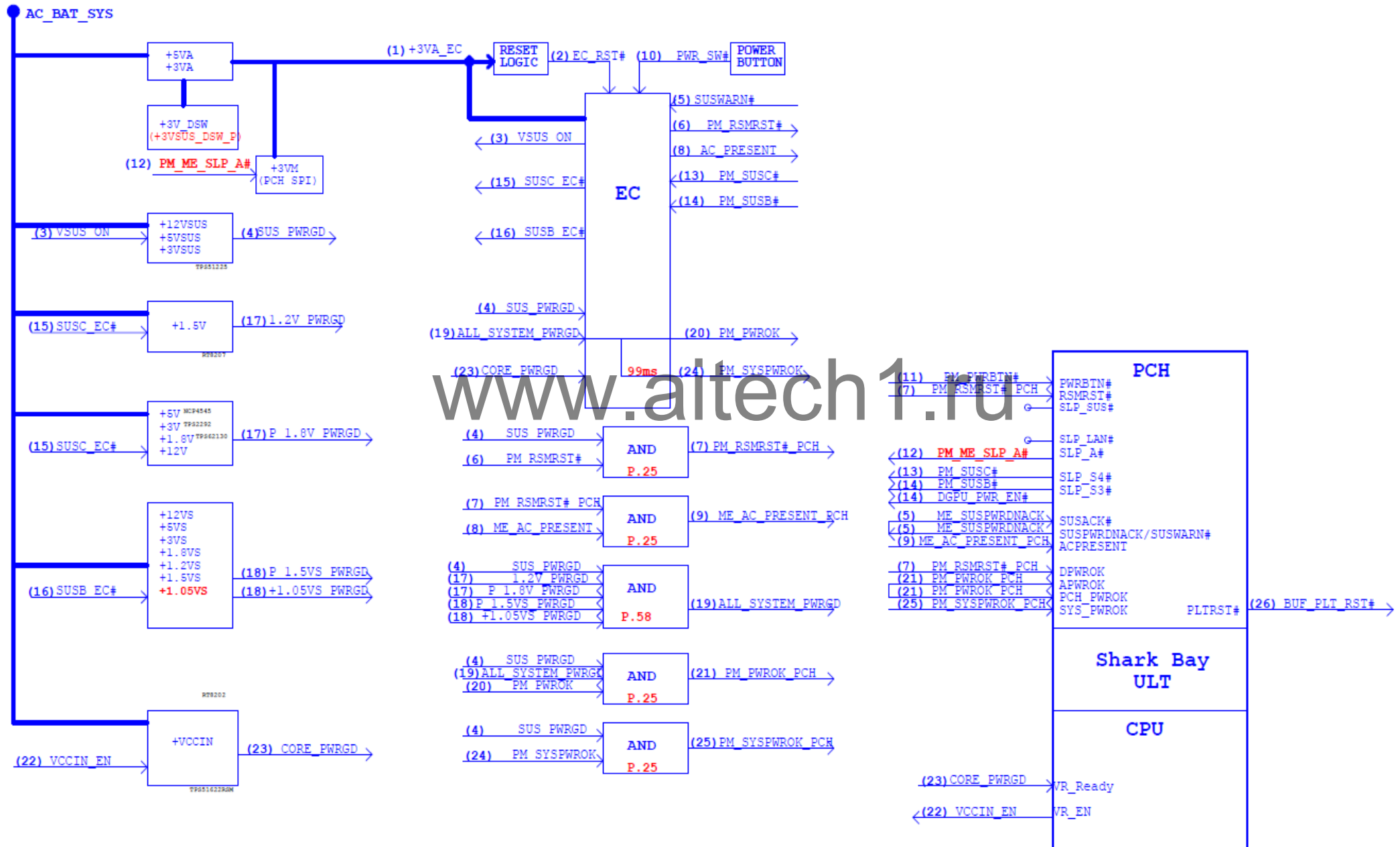
BLOCK DIAGRAM



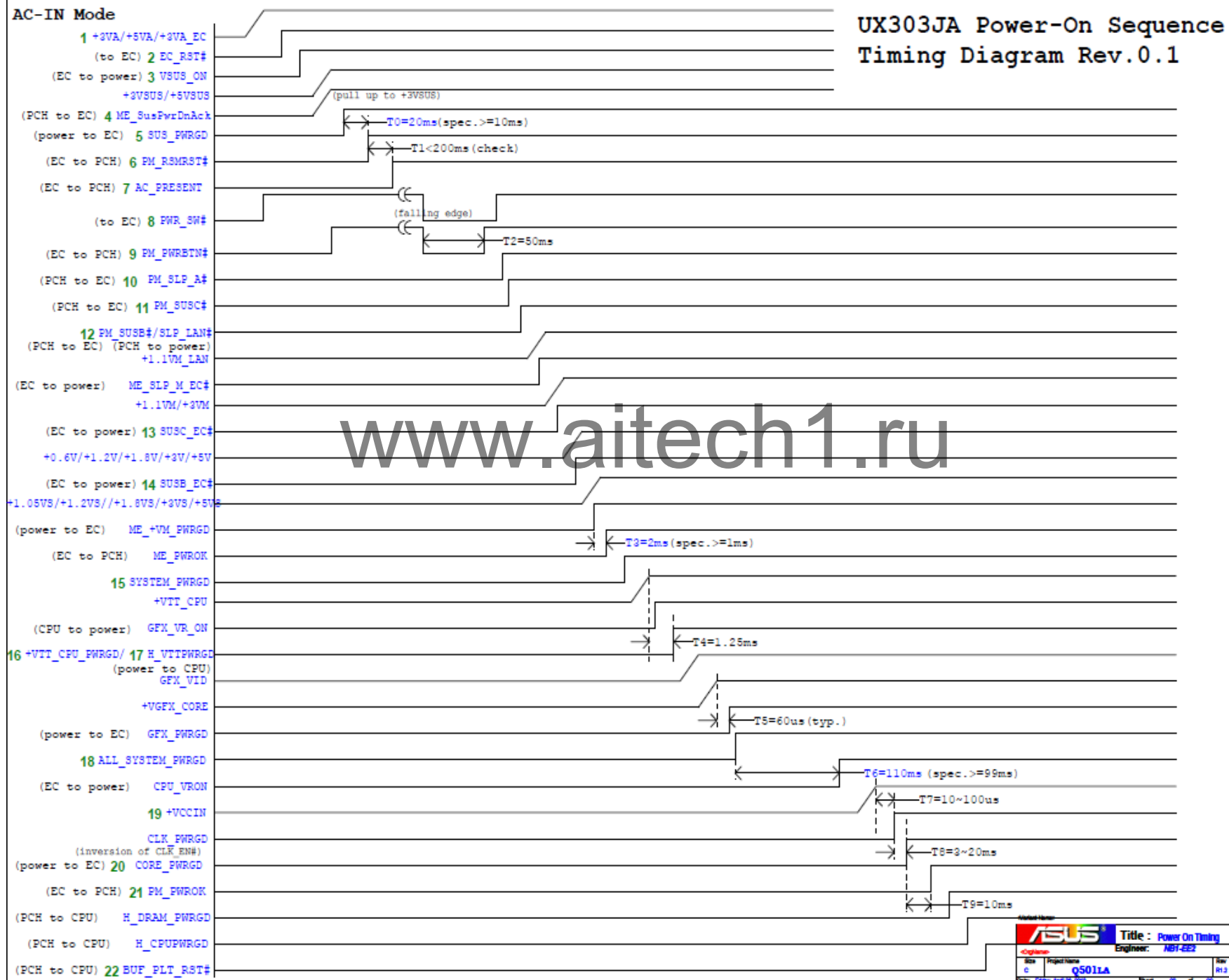
POWER FLOW



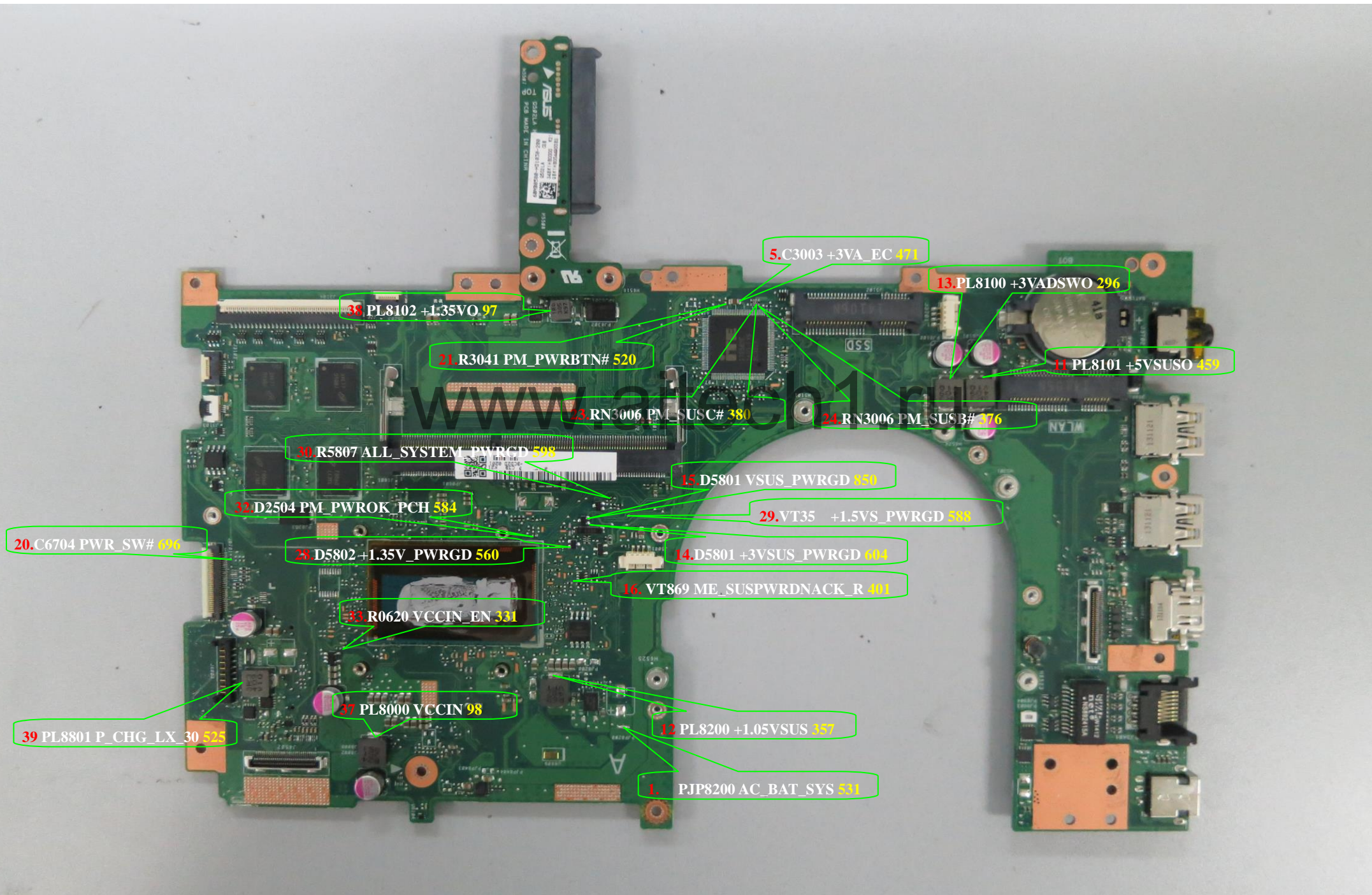
(23) CORE_PWRGD > 99ms (24) PM_SYSPWRK (11) PM_PWRBTN (7) PM_RSTST#



AC POWER ON SEQUENCE



Signal Measure Point-Bottom



Signal Measure Point-Top

